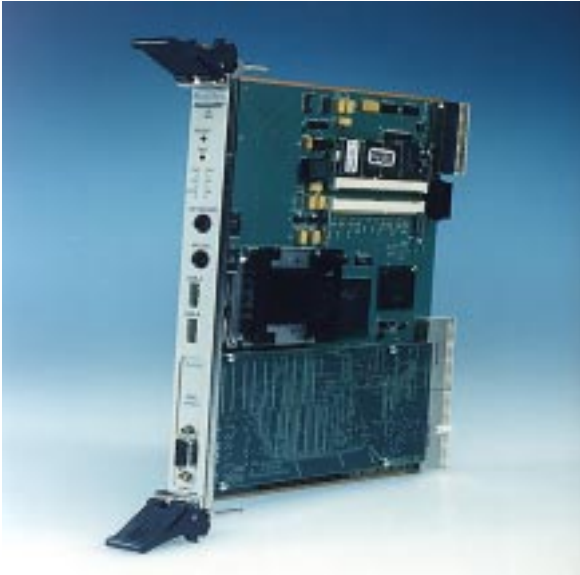


# RadiSys® EPC®-202

One Slot Mobile Pentium® II CompactPCI® Embedded Computer



## System Overview

The one-slot EPC®-202 is designed for the space conscious, high-performance CompactPCI® marketplace. It features J5 access to a 10BaseT and 100BaseTX Ethernet controller, IDE, serial and parallel ports and front panel access to SVGA video, USB, keyboard, mouse, reset and NMI switches. These features combine to make the EPC-200 the indispensable choice for any high-performance, streamlined CompactPCI system.

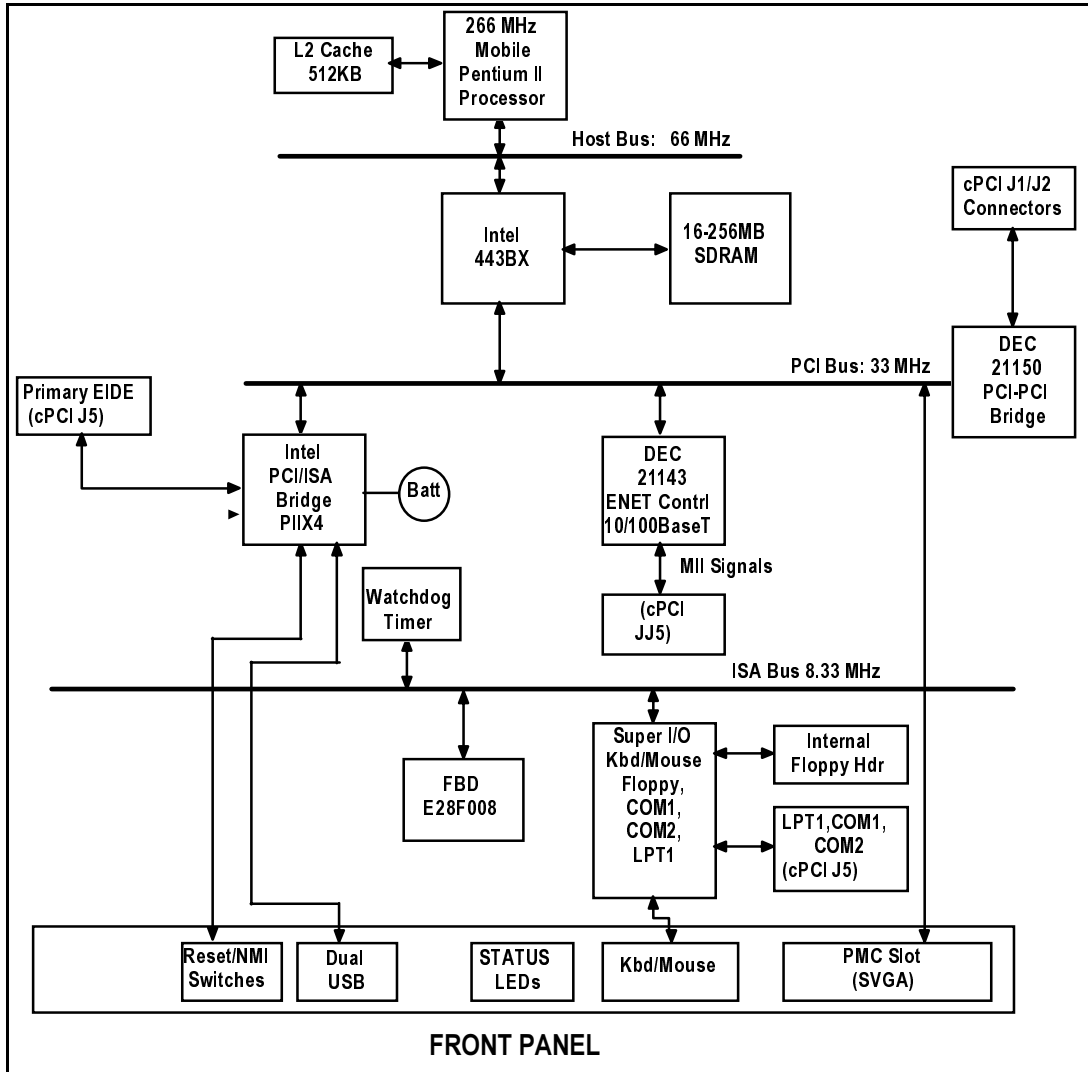
The EPC-202 utilizes the 132MB/s PCI local bus for both on-board and CompactPCI I/O. The DEC 21150 PCI-PCI bridge chip is the interface between the on-board PCI bus and the CompactPCI bus and extends the number of supported CompactPCI peripheral slots to seven.

The EPC-202 is specifically targeted to run Windows NT® 4.0 and has passed the Windows NT hardware compatibility tests.

## Feature Summary

- One Slot CompactPCI board using 266MHz Intel Mobile Pentium® II processor in mini-cartridge format
- 512KB secondary (L2) cache
- Supports up to seven 3U/6U CompactPCI peripheral slots via DEC 21150 PCI-PCI bridge chip
- Two 144-pin SODIMM sockets support up to 256MB DRAM
- Optional SVGA module based on the Cirrus Logic CL-GD5446
- 10/100BaseTX Ethernet controller (MII interface) on board with J5 access (PHY must be supplied via customer system)
- One EIDE channel drives two devices via J5 Backplane
- Reset and NMI switch via front panel
- Parallel port and serial port access vial J5 backplane
- Two USB ports via front panel
- PS/2 style keyboard/mouse connectors on front panel
- Programmable watchdog timer
- On-board header available for speaker
- Full 32-bit CompactPCI interface with J1 and J2 connectors
- Real-time clock with on-board battery
- Floppy support via on-board header
- Serial console redirection support in BIOS (video and keyboard)
- Windows NT 4.0 Support
- Passed Windows NT hardware compatibility tests

### EPC-202 Block Diagram



**SPECIFICATIONS**

<b>FEATURE</b>	<b>FUNCTION</b>	<b>DESCRIPTION</b>
Board Style		One Slot CompactPCI, Eurocard Size "B"
CPU		266MHz Mobile Pentium II processor
Cache		512KB L2 cache using synchronous pipeline burst SRAM
PCI Chipset		Intel 430BX PCIset
System Memory	Capacity	8 to 256MB via two, 144-pin SODIMM SDRAM/EDO modules
	Size	60ns or 70ns 4MB, 8MB, 16MB, 32MB, or 64MB, 3.3V SODIMMs
System BIOS	BIOS Type	Phoenix BIOS with 1024KB flash and battery backed CMOS RAM
	Special Features	PC95 and PCI 2.1 compliant; Windows 95 ready Plug and Play; APM 1.2; IDE drive autoconfigure; multilingual support
Integrated PCI IDE	Number of Devices	One independent channel, two IDE devices, PCI 2.1 compliant
	Connectors	Access through J5 backplane
	Modes	1, 2, 3, 4
Integrated Super I/O	Controller	SMC 37C665IR
	Serial Ports	Via J5 backplane
	Parallel	Via J5 backplane
	Floppy Controller	34-pin header on-board; 2.88MB, 765A compatible
	Keyboard Controller	8042-compatible
RTC	Real Time Clock	Accurate to $\pm 13$ minutes/yr
	Battery	Field replaceable 3.0V, 200mAH CR2032 lithium battery
Ethernet	10/100BaseTX	Digital 21143 for 10/100Mbit Ethernet (MII interface only, PHY must be supplied by customer)
PMC Slot	PCI Mezzanine Card	One PMC module slot available for I/O (adheres to IEEE P1386.1, PCI mezzanine card)
Optional SVGA Graphics Module	Form Factor	Optional Video PMC module (adheres to IEEE P1386.1, PCI mezzanine card)
	Resolution	640 x 480/1280 x 1024
	Chip Set	Cirrus Logic CL-GD5446
Connectors, Front Panel	Keyboard/Mouse	Two IBM PS-2 style 6-pin mini DIN connector
	USB	Two Universal Serial Bus connectors
	PMC slot / Optional Video	One 15-pin SVGA-style connector on optional PMC video module.
	NMI & Reset Switches	Two reset switches
Connector, Headers		Floppy
Watchdog Timer		A retriggerable one-shot WDT with a programmable reset interval ranging from 128 milliseconds to 8.2 seconds
Environment	Operating Temperature	EPC202-266 $\Rightarrow$ TBD°C - TBD°C
Power Requirement	Typical w/ SVGA, 32MB Memory	EPC202-266 $\Rightarrow$ TBD A @ 3.3V DC
		All $\Rightarrow$ TBD A @ 5V DC
		All $\Rightarrow$ <TBD mA @ 12V DC

**ORDER CODES**

EPC202-266                    266MHz Pentium II Processor, 32MB  
**EPC202V-266    266MHz Pentium II Processor, SVGA PMC Module, 32MB**



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