



Specifications and Features

Analog Input

Channel: 16-channel dual-end
Resolution: 16-bit
FIFO Size: 4K x 20-bit
Sampling Rate: 75Ksps
Current Input Range: $\pm 20\text{mA}$
Input Impedance: 100 Ω
Trigger Mode: Software trigger, internal programmable counter trigger and external trigger
Accuracy: 0.02% FSR

Programmable Timer/Counter

Channel: 3-channel (1-channel independent, 2-channels multiplexed with digital input/output)
Bit: 16-bit
Count Method: Count-down
Clock Input Method: Internal input or external input
Base Clock:
Internal: 10MHz
External: 10MHz (max.)
Input Voltage:
Clock Input: $V_{IL(MAX)}=0.8\text{V}$, $V_{IH(MIN)}=2.0\text{V}$
Gating Input: $V_{IL(MAX)}=0.8\text{V}$, $V_{IH(MIN)}=2.0\text{V}$
Output voltage: $V_{OH(MIN)}=2.4\text{V}@8\text{mA}$, $V_{OL(MAX)}=0.5\text{V}$
Compatibility: Compatible with TTL

Digital Input

Channel: 16-channel
Input Voltage: $V_{IH(MIN)}=2.0\text{V}$, $V_{IL(MAX)}=0.8\text{V}$
Input Method: Supports both dry and wet contact input
Compatibility: Compatible with TTL

Digital Output

Channel: 16-channel
Output Voltage: $V_{OH(MIN)}=2.4\text{V}@8\text{mA}$, $V_{OL(MAX)}=0.5\text{V}$

General Specifications

4-bit Board ID setup
External Dimensions (L x W): 175mm x 106mm (6.9" x 4.2")
Power Consumption:
Typical: +5V @XXXmA
Max: +5V @XXXmA
Operating Temperature: 0°C~60°C
Storage Temperature: -20°C~+70°C
Relative Humidity:
Operating: 5%~85%RH, non-condensing
Storage: 5%~95%RH, non-condensing

Ordering Information

Part Number	Model Number	Description
0060-003510	PCI-16AD16A	It is a data acquisition card with 16-channel current input, 16-channel digital input and output, 3-channel 16-bit programmable timer/counter
0060-001900	PCLD-880	DB-37 port industrial terminal board