



Coming soon...

Specifications and Features

Analog Output (DA)

Channel: 4-channel
Resolution: 16-bit
FIFO Size: 8K x 16-bit

Trigger Method

Continuous Method: A. (via FIFO) Interrupt mode; continuous output
B. (via FIFO) Non-interrupt mode; continuous output
One-time Method: Software controls analog signal output via operating DA output register

Digital Input and Output

Channel: 1-channel
Compatibility: Compatible with TTL
Input Level: $V_{IH(MIN)}=2.0V$, $V_{IL(MAX)}=0.8V$
Output Voltage: $V_{OH(MIN)}=4.5V @ 24mA$, $V_{OL(MAX)}=0.55V$
Throughput: 1MHz

Supports dry contact and wet contact input

Interrupt: Each group of input connector can generate interrupt
Interrupt Method: External event trigger, mode match, status change

Voltage Output

Output Range: Unipolar: 0~5V, 0~10V
Bipolar: -5~5V, -10~10V
Output Slew Rate: 11V/ μ s
Output Impedance: $\leq 0.2\Omega$
Output Driver: 3mA (max.)
Accuracy: 0.02% FSR (FSR: Full Scale Range)
Clock Selection: Internal/external

Programmable Timer/Counter

Channel: 1
Resolution: 16-bit
Count Type: Reduced count
Clock Type: External or internal
Max. Input Frequency: 20MHz
Compatibility: Compatible with TTL
Interrupt: Generate interrupt after count
Clock Input: $V_{IH(MIN)}=2.0V$, $V_{IL(MAX)}=0.8V$
Clock Input: $V_{IH(MIN)}=2.0V$, $V_{IL(MAX)}=0.8V$
Counter Output: $V_{OH(MIN)}=4.5V@24mA$, $V_{OL(MAX)}=0.55V$

General Specifications

2-bit Board ID setup function
Typical Power Consumption
+5V@120mA; +5%/-3%
+12V@150mA; +5%/-3%
Max. Power Consumption
+5V@180mA; +5%/-3%
+12V@200mA; +5%/-3%
External Dimensions (L x W): 96mm x 90mm (3.775" x 3.550")
Operating Environment:
Temperature: 0°C~60°C
Humidity: 5%~85% (non-condensing)
Storage Environment
Temperature: -20°C~70°C
Humidity: 5%~95% (non-condensing)

Ordering Information

Part Number	Model Number	Description
0060-003310	104P-4DA16	4-channel with 16-bit D/A analog output, 16-channel digital input/output channel and 1-channel timer/counter
0060-001900	PCLD-880	DB-37 port industrial terminal board